



Attorney Docket No. 1934.1001 (formerly 1687.1005)

ajc

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hiroyuki YAMASHITA, et al.

Application No.: 10/766,955

ATTENTION

Confirmation No.: 2505

CERTIFICATE OF CORRECTION

Filed: January 30, 2004

BRANCH

U.S. Patent No.: 7,155,690

Issued: December 26, 2006

For: A METHOD FOR CO-VERIFYING HARDWARE AND SOFTWARE FOR A  
SEMICONDUCTOR DEVICE (As Amended)

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Certificate  
of Correction  
MAY 10 2007

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: May 8, 2007

By: [Signature]  
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MAY 10 2007

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 7,155,690

DATED : December 26, 2006

INVENTOR(S) : Hiroyuki YAMASHITA, et al.

Column 10, Line 38, change "teatbench" to --testbench--.

Column 11, Line 9, change "teatbench" to --testbench--.

Column 11, Line 11, change "teetbench" to --testbench--.

Column 11, Line 53, change "interruct" to --interrupt--.

Column 12, Line 16, change "The" to --the--.

Column 12, Line 41, change "end" to --and--.

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PATENT NO. 7,155,690

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